

Wenbin Huang

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Education

Shanghai Jiao Tong University, Shanghai, China

B.S. in Computer Science, Expected June 2027.

Selected into the **ACM Honors Class**, a highly competitive program for top CS students.

GPA: 3.69 / 4.3 (\approx 3.43 / 4.0); **Core GPA:** 3.71 / 4.3 (\approx 3.45 / 4.0); **Core Average Score:** 88.1 / 100

Research Experience

X-LANCE Lab Shanghai Jiao Tong University Jun. 2025 - Present

Research Internship Advisor: **Prof. Kai Yu**

- Speech Intelligence, AudioLLM, Audio-Text understanding, ASR

Publications

- Bohan Li*, **Wenbin Huang***, Yuhang Qiu*, et al.
“**ISA-Bench: Benchmarking Instruction Sensitivity For Large Audio Language Models**”
Accepted by IEEE ICASSP 2026. (Co-first author, Oral Presentation)
- Wenbin Huang***, Yuhang Qiu*, et al.
“**RAS: a Reliability Oriented Metric for Automatic Speech Recognition**”
arXiv preprint. (Co-first author)

Honors & Awards

Zhiyuan Honors Program Top 10% students in SJTU Sept. 2023 - Present

Zhiyuan Honor Scholarship 2023, 2024 and 2025

Personal Projects

Tomasulo CPU (Verilog) Project of **Computer Architecture** Fall 2024

- Designed and implemented a RISC-V CPU with Tomasulo algorithm with branch predicting and instruction cache.
- Supported instructions in rv32ic ISA.

Java-like Compiler (Java) Project of **Compiler Design and Implementation** Summer & Fall 2024

- Designed and implemented a compiler for a Java-like language for education, including syntax & semantic checking, code generation and IR optimization.
- Implemented optimizing algorithms including M2mReg, Register Allocation, Function Inlining, Dead Code Elimination, Global Variable Localizing and so on.

Skills

Programming Python, C++, Java; PyTorch, HuggingFace Transformers

Tools Git, Docker, Conda, Shell, LaTeX, Markdown, Typst

Languages Chinese (Native Speaker), English (Fluent)